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ABSTRACT

A method is provided to reduce strapping devices in a computer system having at least one configurable device, which includes the following steps. A configuration value stored in a non-volatile memory is first provided. During power-up and reset of the computer system, a processor reset signal and a bus reset signal of a highspeed peripheral bus are both asserted, wherein the highspeed peripheral bus is included in the computer system. When an operation clock of the high-speed peripheral bus and frequency, voltage working its reaches configuration value is fetched from the non-volatile The fetching step is repeated until a most significant bit (MSB) of a fetched configuration value second a state to first from a Subsequently, the configuration value fetched from the non-volatile memory is asserted to the at least one configurable device to configure the configurable device, and then the processor reset signal is deasserted, and one configurable device is thereby at the completely configured.